

S/N 10/612,293

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Daniel A. Day

Examiner: Phung M. Chung

Serial No.: 10/612,293

Group Art Unit: 2117

Filed: June 30, 2003

Docket No.: 884,879US1

Title: Selective Control of Test-Access Ports in Integrated Circuits

Assignee: Intel Corporation

Customer Number: 21186

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

This responds to the Office Action mailed on July 23, 2007. Please amend the above-identified patent application as follows.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a two-month extension of the period for responding to the Office action, thereby moving the deadline for response from October 23, 2007 to December 23, 2007.